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## STMicroelectronics Joins ZETA Alliance to Promote Emerging Long-Range IoT Connectivity Standard

- ZETA advanced LPWAN technology extends opportunities for low-power, low-cost smart-living applications
- ❖ ZETA protocol on <u>STM32WL\* wireless microcontrollers</u> to aid product development

**Geneva, January 26, 2021 – STMicroelectronics (NYSE: STM)**, a global semiconductor leader serving customers across the spectrum of electronics applications, has joined the ZETA Alliance, the industry body promoting ZETA Low-Power Wide Area Network (LPWAN) technology for low-cost long-range IoT connectivity.

ZETA technology is quickly becoming established throughout China, Japan, and beyond, bringing together innovative wireless technologies that enable low-power, low-cost devices to rely on robust connections over extended distances. In addition, native support for mesh networking, which allows peer-to-peer communication among the network nodes, boosts network coverage and resilience.

Joining the Alliance as a Promoter member, ST expects ZETA technology to further accelerate the spread of IoT in all territories worldwide. The standard lets developers create high-value IoT-based solutions to challenges that face extreme cost constraints.

"ZETA has a strong value proposition and is taking its place among established LPWAN technologies, boosting choice and flexibility for solution developers and enabling the IoT to deliver even greater benefits to more end users," said Hakim Jaafar, STM32 Wireless Marketing Director, STMicroelectronics. "ST is actively engaged with the industry bodies for all leading LPWAN standards for IoT connectivity and offers a portfolio of solutions that help developers bring their innovations to market quickly and cost-effectively."

Welcoming ST to the ZETA Alliance, Dr. Zhuoqun Li, CEO of Zifisense, a major contributor to the ZETA technology and a founding member of the ZETA Alliance, said, "We believe ST will greatly strengthen the ZETA developer ecosystem chain and promote further evolution and deployment of ZETA technology. We look forward to working with ST to create a broader LPWAN 2.0 global IoT eco-system."

ST is working with ZiFiSense to port the technology to the highly integrated STM32WL wireless System-on-Chip (SoC) devices, which combine ultra-low-power microcontroller functionality with a wireless stage certified to international radio-equipment regulations on the same chip.

## **Further technical information**

ZETA leverages an advanced M-FSK modulation and coding scheme created by ZiFiSense that allows the system to detect and demodulate signals from transmitters of extremely low power. M-FSK receiver sensitivity can get as low as -150dBm. Hence ZETA can achieve extremely long wireless-communication range with low transmitter output power.

The STM32WL SoC provides a convenient, power-efficient, and secure platform on which to develop IoT devices to leverage <u>LPWAN connectivity</u>. In addition to integrating the low-power microcontroller and radio front end on the same silicon die, which reduces the solution footprint and lowers the bill of materials, these devices permit further savings by requiring only a single crystal to synchronize the high-speed external (HSE) clock and radio. The SoC delivers up to 22dBm RF output power without an external power amplifier. An alternative energy-optimized power output can deliver up to 15dBm. Such dual power output and a linear frequency range from 150MHz to 960MHz make the chip suitable for any geographic market.

As a worldwide compatible open platform, the STM32WL supports LoRa modulation scheme, as well as (G)FSK, (G)MSK and BPSK modulation used by <u>ZETA</u>, Sigfox, wireless Meter-Bus (wM-Bus), and many other proprietary protocols. ST enables a rich ecosystem of hardware development tools, software tools, and embedded software packages including LoRaWAN and Sigfox protocol stacks, ready to run out of the box on STM32WL SoCs.

ST's other embedded products suited to use in equipment for LPWA networks include <a href="STM32MP1 microprocessors">STM32MP1 microprocessors</a> that combine an Arm® Cortex®-A7 application core and Cortex-M4 microcontroller core with powerful peripherals that are ideal for gateway devices, as well as <a href="STM32L0">STM32L0</a> and <a href="STM32L4">STM32L4</a> ultra-low-power microcontrollers that let battery energy go further in network endpoints.

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