

Almere, The Netherlands

December 6, 2019

ASM INTERNATIONAL TO HOST TECHNOLOGY SEMINAR

ASM International N.V. (Euronext Amsterdam: ASM) today announces that it will host a technical luncheon seminar in San Francisco, CA, US, on Wednesday, December 11, 2019, the third day of the IEDM Conference.

At this technology seminar, ASM will highlight the challenges and potential solutions for metallization applications.

The agenda is as follows:

11:30 am	Reception, food and drinks
12:00 - 12:15 pm	Welcome and introduction
12:15 - 1:00 pm	Invited speaker: Dr. Raghuvveer Makala (Western Digital) - "Metallization Challenges in 3D NAND Flash"

Following the presentations, there is an opportunity for open discussion and networking until 1:15 pm.

The ASM technology seminar will take place in the Golden Gate room (25th floor) at the Nikko Hotel (across from the Hilton San Francisco), San Francisco, CA 94102. The room will open at 11:30 am for invited attendees. Interested parties should contact Rosanne de Vries, +31 88 100 8569, Rosanne.de.Vries@asm.com.

About ASM International

ASM International NV, headquartered in Almere, the Netherlands, its subsidiaries and participations design and manufacture equipment and materials used to produce semiconductor devices. ASM International, its subsidiaries and participations provide production solutions for wafer processing (Front-end segment) as well as for assembly & packaging and surface mount technology (Back-end segment) through facilities in the United States, Europe, Japan and Asia. ASM International's common stock trades on the Euronext Amsterdam Stock Exchange (symbol ASM). For more information, visit ASM's website at www.asm.com.

Cautionary Note Regarding Forward-Looking Statements: All matters discussed in this press release, except for any historical data, are forward-looking statements. Forward-looking statements involve risks and uncertainties that could cause actual results to differ materially from those in the forward-looking statements. These include, but are not limited to, economic conditions and trends in the semiconductor industry generally and the timing of the industry cycles specifically, currency fluctuations, corporate transactions, financing and liquidity matters, the success of restructurings, the timing of significant orders, market acceptance of new products, competitive factors, litigation involving intellectual property, shareholders or other issues, commercial and economic disruption due to natural disasters, terrorist activity, armed conflict or political instability, epidemics and other risks indicated in the Company's reports and financial statements. The Company assumes no obligation nor intends to update or revise any forward-looking statements to reflect future developments or circumstances.

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